

FIG. 1

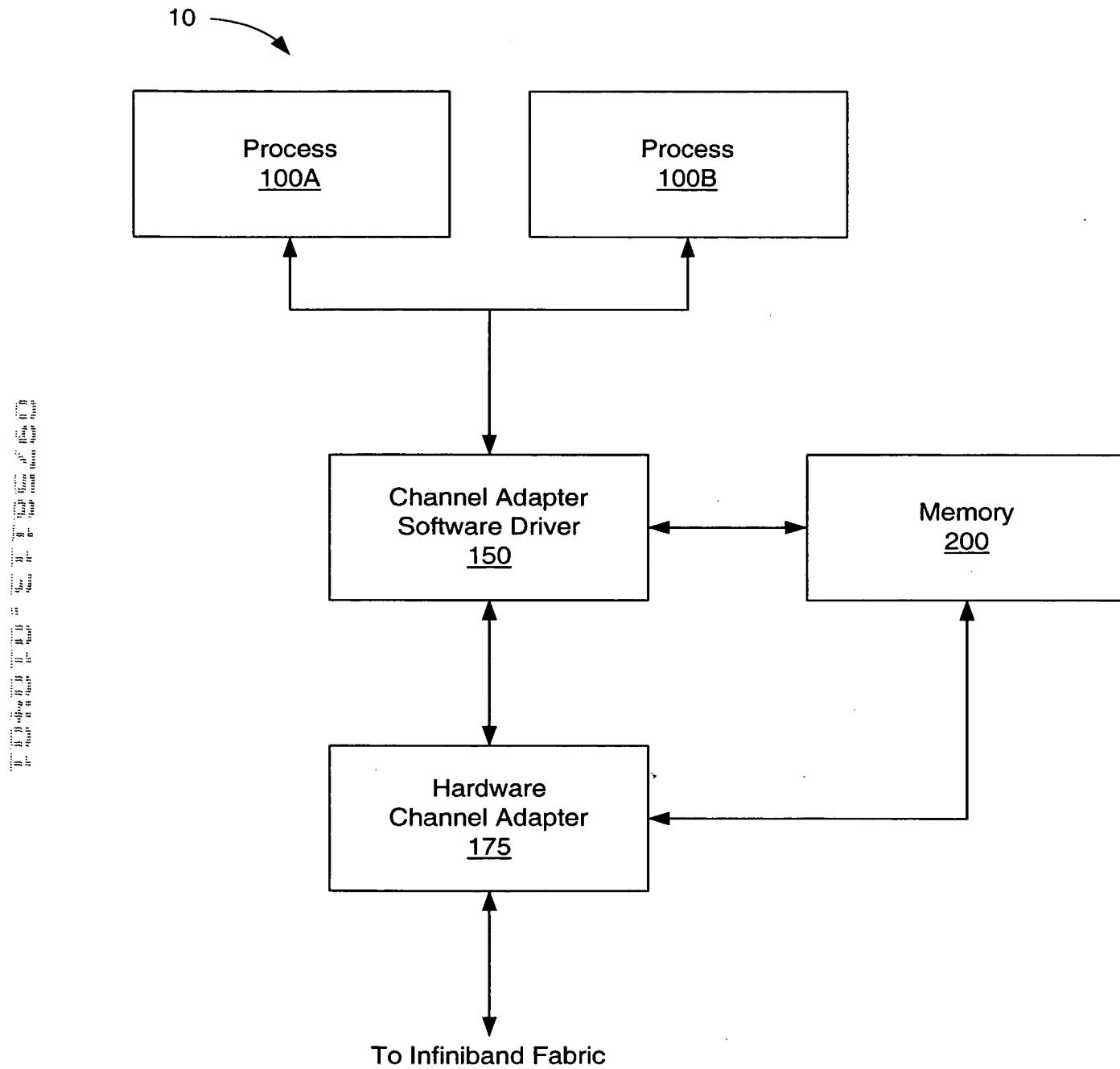


FIG. 2

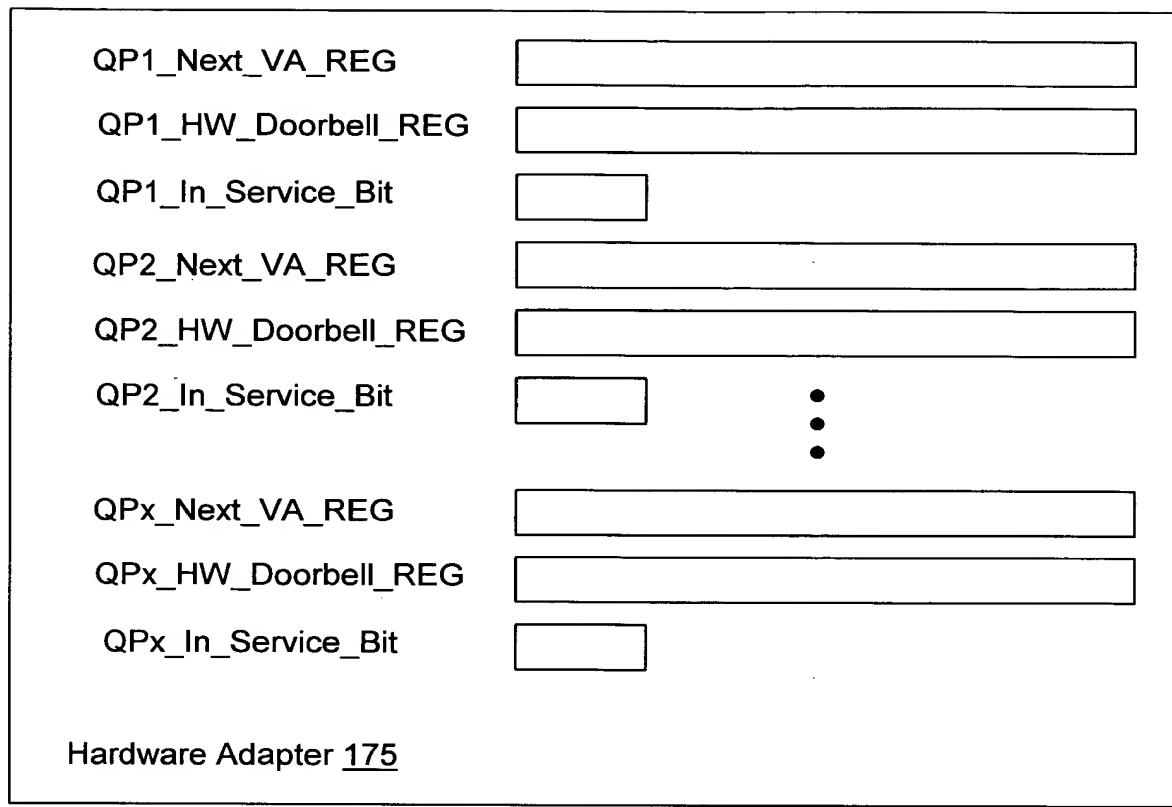


FIG. 3A

Virtual Address	Work Queue Element Information	Next Virtual Address	Next WQE Posted	Busy
WQE_VA0		0	0	0
WQE_VA1		0	0	0
WQE_VA2		0	0	0
WQE_VA3		0	0	0
QPx Driver Last WQE Accessed Register <u>215</u>			QPx 210	

FIG. 3B

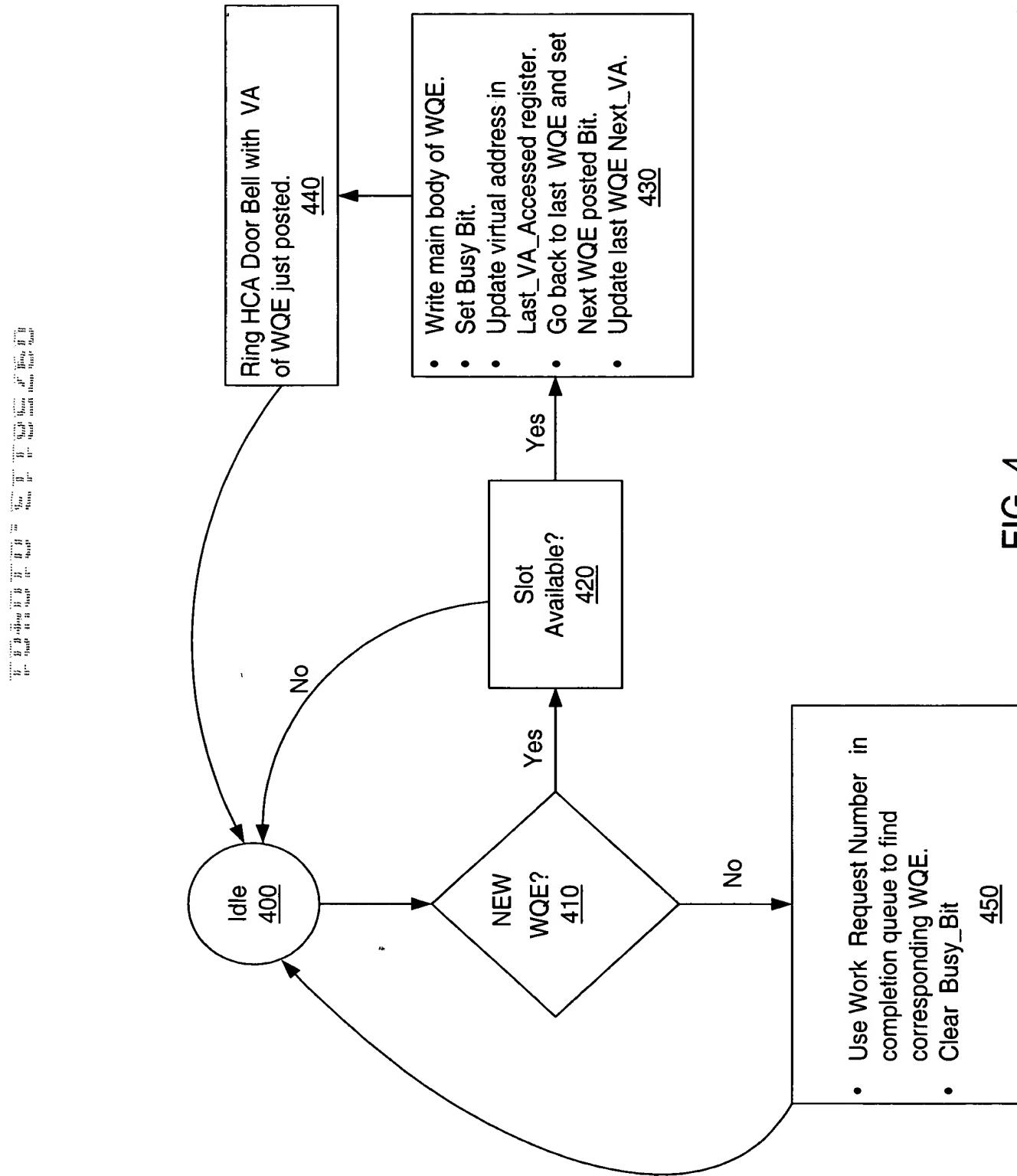


FIG. 4

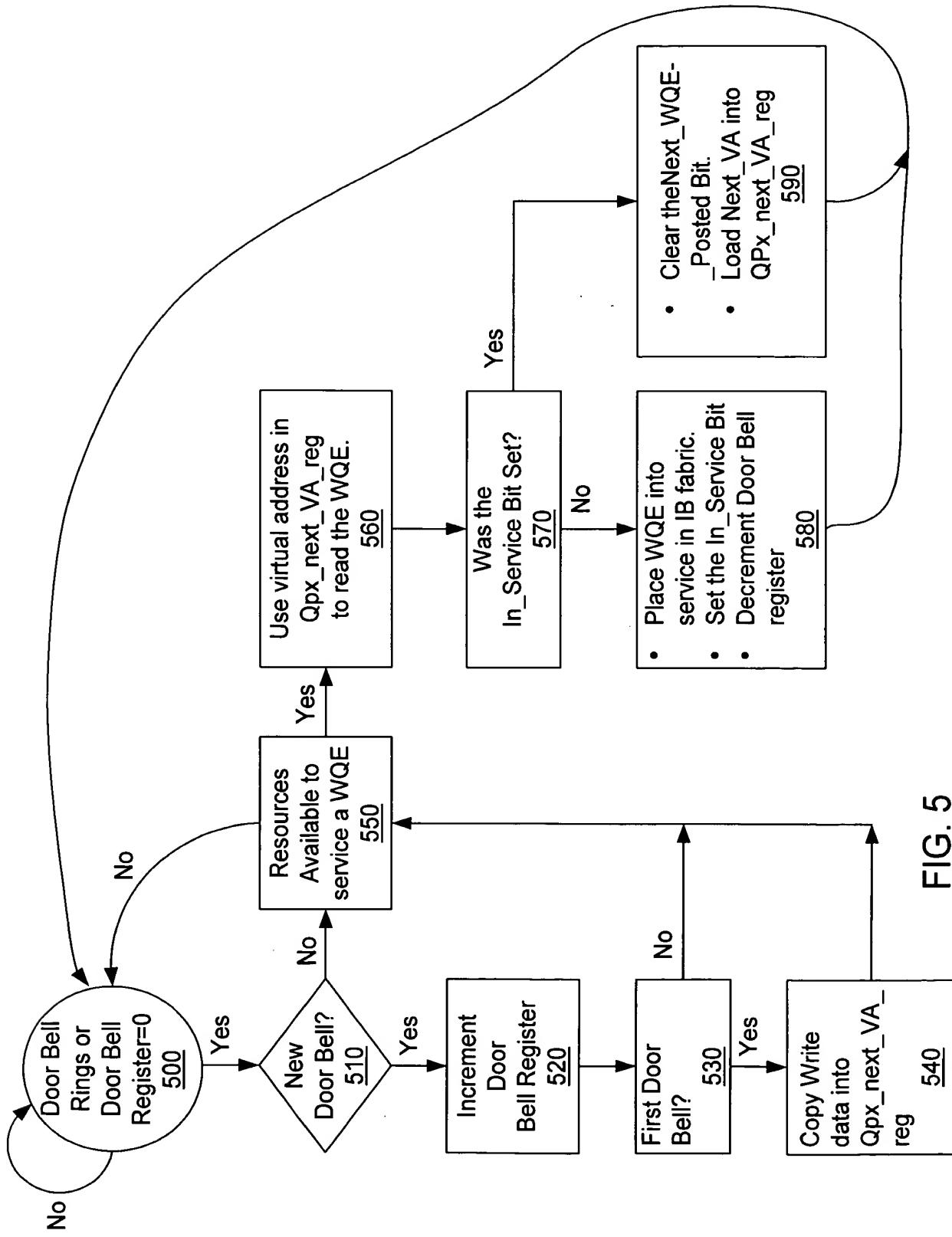


FIG. 5